

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

STEPHEN H. GUNTHER, ET AL.

Application No.:

Filed: April 9, 2004

For: **METHODS AND APPARATUS FOR THERMAL
MANAGEMENT OF AN INTEGRATED CIRCUIT DIE**

Art Group: 2863

Examiner: Tung S. Lau

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
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Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Continuation Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No. 09/784,255, filed February 14, 2001) and copies of the references were furnished at that time. Accordingly, per 37 CFR §1.98(d)(1) additional copies of those references are not submitted herewith.

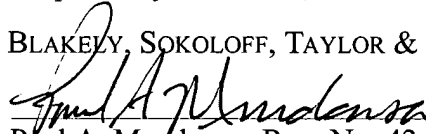
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: April 09, 2004


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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | Complete if Known | |
| | | | | Application Number | |
| | | | | Filing Date | April 9, 2004 |
| | | | | First Named Inventor | Stephen H. Gunther |
| | | | | Art Unit | 2863 |
| | | | | Examiner Name | Tung S. Lau |
| Sheet | 4 | of | 4 | Attorney Docket Number | 42P4728XC |

| NON PATENT LITERATURE DOCUMENTS | | | | |
|---------------------------------|-----------------------|---|--|--|
| Examiner Initials* | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | |
| | 48 | SHU-YUAN CHIN & CHUNG-YU WU. " A New Type of Curvature-Compensated CMOS Bandgap Voltage References', VLSITSA, 1991, Pages 398-402. | | |
| | 49 | MARCO FERRO,FRANCO SALERNO & RINALDO CASTELLO, " A Floating CMOS Bandgap Voltage References for Differential Applications". IEEE: Journal of Solid-State Circuits. Vol 24 no. 3, June 1989, Pages 690-697 | | |
| | 50 | O. SALMINEN, K HALONEN, "The Higher Order Temperature Compensation of Bandgap Volt References", IEEE, 1992, Pages 1388-1391 | | |
| | 51 | HECTOR SANCHEZ, ET AL. " Thermal Management System for High Performance PowerPC Microprocessors", IEEE, COMPCON '97, February 1997, Pages 325-330. | | |
| | 52 | PHILLIP E. ALLEN & DOUGLAS R. HOLBERG, " CMOS Analog Circuit Design", HRW, Pages 529-551 | | |
| | 53 | " Low Power, Programmable Temperature Controller", Rev. C, Analog Devices, Inc., 1995 Pages 1-16. | | |
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¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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